# Getting Started with LPC5528 Wireless Gamepad Solution

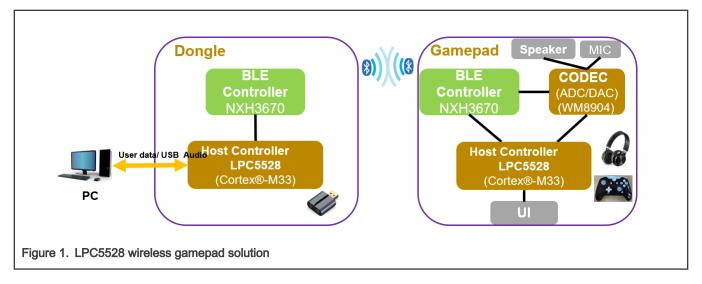
Rev. 1 — 21 May, 2021 Application Note

# 1 Introduction

This document introduces the wireless gamepad solution based on LPC5528 and NxH3670. It provides some necessary information to guide developers how to use the software and hardware resources provided by NXP to develop wireless gamepad or wireless headset. The design materials of this solution will be released on NXP website together with this document. Figure 1 shows the system block diagram of this solution.

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# 2 Getting started

This section introduces the hardware and software resources required by this solution.

### 2.1 Hardware

This solution requires two boards: LPC5528 gamepad main board and LPC5528 dongle board.



## 2.1.1 LPC5528 gamepad main board and dongle board features

The gamepad main board supports the following features:

- LPC5528JEV98 MCU (Arm<sup>®</sup> Cortex<sup>®</sup>-M33 core, 150 MHz clock, 512 KB Flash, 256 KB RAM, 7 × 7 mm<sup>2</sup> VFBGA98 package)
- NxH3670UK (Arm Cortex-M0 core, Ultra-low Power, Low Latency Audio for Wireless Gaming Headphone)
- WM8904 Codec
- · Battery charge
- · 3.5 mm headphone jack
- · High Speed USB interface
- · SWD interface
- · Reset button, two user buttons
- · Joysticks and buttons

The dongle board supports the following features:

- LPC5528JBD64 MCU (Arm Cortex-M33 core, 150 MHz clock, 512 KB Flash, 256 KB RAM, HTQFP package)
- NxH3670UK (Arm Cortex-M0 core, Ultra-low Power, Low Latency Audio for Wireless Gaming Headphone)
- · High Speed USB interface
- · SWD interface
- · Reset button. ISP button

LPC5528 gamepad main board is as shown in Figure 2 and Figure 3.

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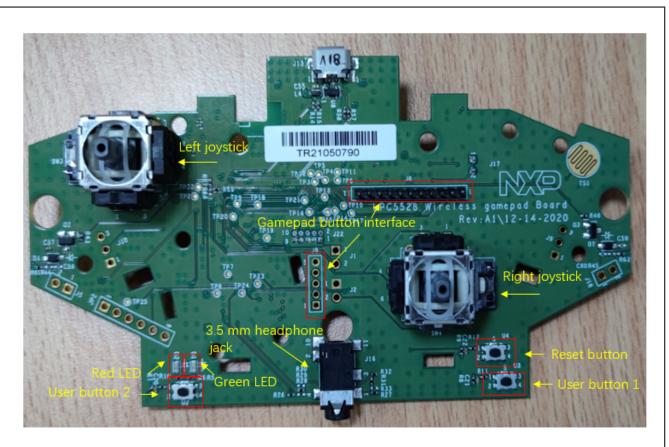


Figure 2. Front view of gamepad main board

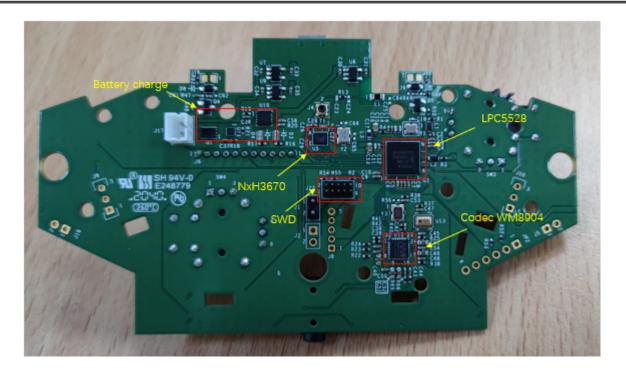


Figure 3. Back view of gamepad main board

LPC5528 dongle board is as shown in Figure 4.

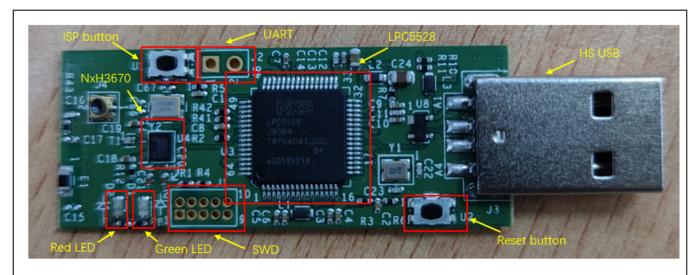


Figure 4. LPC5528 dongle board

## 2.2 Software

This solution is developed based on NxH3670 SDK Rev5.2 and LPC5528 SDK v2.7.1. NXP provides the NxH3670 SDK board and the corresponding software development kit. The NxH3670 SDK can be downloaded from NxH3670 SDK Gaming Package. The original host controller on the NxH3670 SDK board is KL27. But in this solution, we use high performance LPC5528 instead of KL27. This LPC5528-based solution allows enough room for users to expand their own functions.

## 2.2.1 Original NxH3670 SDK

The structure of original NxH3670 SDK Rev5.2 is as shown in Figure 5.

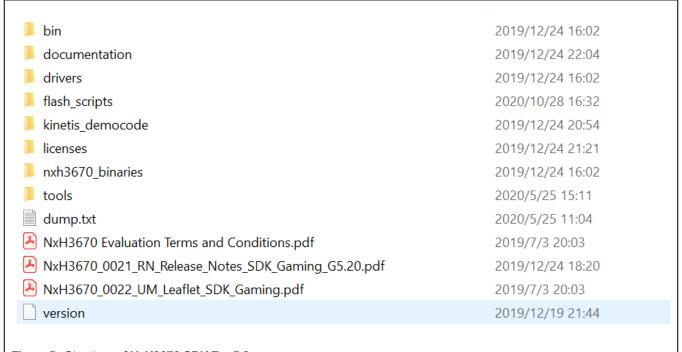
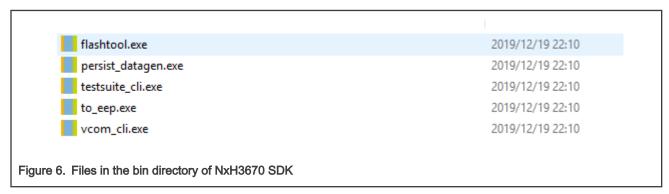


Figure 5. Structure of NxH3670 SDK Rev5.2

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The content in the SDK package is as follows:

· bin: Some executable files necessary for the execution of GUI tools, as shown in Figure 6.



- flash\_scripts: Flash scripts are used to generate dongle and headset device information and pairing data.
- · document: Some useful document for developers.
  - NxH3670\_0027\_UM\_HAPI\_SysRadioTest.pdf
  - NxH3670\_0029\_AN\_Performance.pdf
  - NxH3670\_0033\_AN\_Pre\_Production\_Steps.pdf
  - NxH3670\_AN11953\_HAPI\_Bootloader.pdf
  - NxH3670\_AN12244\_RadioIntegration.pdf
  - NxH3670\_AN12280\_Reference\_Application\_Software\_Architecture.pdf
  - NxH3670 AN12360 Gaming.pdf
  - NxH3670\_AN12361\_OTA\_Firmware\_Upgrade.pdf
  - NxH3670 AN12362 TestSuite.pdf
  - NXH3670\_HAPI\_overview.pdf
  - NxH3670\_UM11148\_HAPI\_Gaming.pdf
  - NxH3670\_UM11149\_Host\_Interface.pdf
  - NxH3670 UM11198 FlashTool.pdf
  - NxH3670\_UM11203\_HAPI\_OTA.pdf
  - NxH3670-0032\_UM\_HAPI\_VCOM\_Rev0.6.pdf
  - NxH3670UK\_Datasheet.pdf

Figure 7. Documentations provided in the NxH3670 SDK package

- kinetis\_democode: Including the app, ota\_app, and SSB projects required by the headset and dongle.
- nxh3670\_binaries: The firmware of NxH3670 required by dongle and headset.

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tools: Some useful tools include Nxh3670 Flash Tool and VCOM Tool. For the details of tool usage, see NxH3670 FlashTool (document UM11198) and NxH3670 HAPI VCOM.

#### 2.2.2 LPC5528\_NxH3670 SDK

Since this solution is developed based on the NxH3670 SDK and the KL27 host controller is replaced with LPC5528, it is necessary to replace the KL27 SDK with the LPC5528 SDK. In order to improve the development efficiency, this solution reuses the file structure in Figure 5 and replaces kinetis\_democode with lpc\_democode. The development tool is Keil v5.28 IDE. The content of the modified NxH3670 SDK for LPC5528, LPC5528 NxH3670 SDK, is as shown in Figure 8.

📙 bin	2020/11/13 14:47	File folder
documentation	2020/11/16 9:31	File folder
drivers	2020/11/16 9:33	File folder
flash_scripts	2020/11/13 14:47	File folder
licenses	2020/11/16 9:31	File folder
Ipc_democode	2020/11/13 14:47	File folder
nxh3670_binaries	2020/11/13 14:48	File folder
tools	2020/11/13 19:26	File folder

Compared with original NxH3670 SDK, the main differences are as follows:

• Use <code>lpc\_democode</code> to replace <code>kinetis\_democode</code>. <code>lpc\_democode</code> contains the Secondary Stage Bootloader (SSB), applications (app), and ota applications (ota app) of the dongle and gamepad.



• tools: Add a LPC5528\_NxH3670 Flash Tool. See LPC5528\_NxH3670 Flash tool for the details of tool usage.

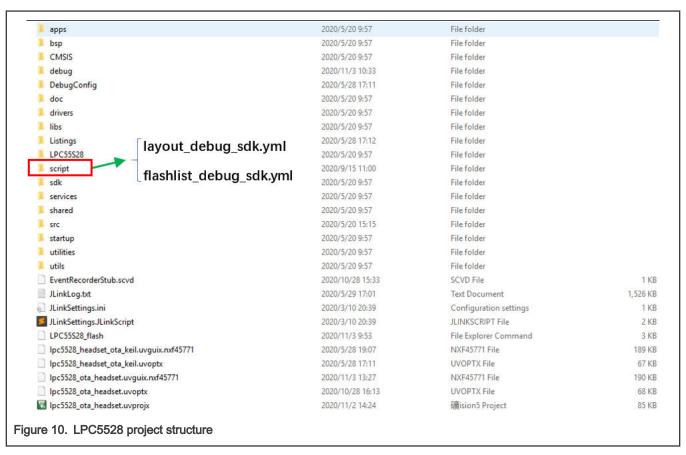
# 3 Firmware development

This section introduces how to use LPC5528\_NxH3670 SDK to develop the wireless gamepad. Before starting to debug the project, developers need to understand the Flash layout and GUI tools usage.

# 3.1 Flash layout

This section introduces two very important files, <code>layout\_debug\_sdk.yml</code> (layout file) and <code>flashlist\_debug\_sdk.yml</code> (flashlist file). These two files are stored in the script folder of each project directory, as shown in Figure 10.

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Layout file and flashlist file are very important files. When using LPC5528\_NxH3670 Flash tool and NxH3670 Flash tool for firmware download and OTA upgrade, the correct .yml file must be provided.

# 3.1.1 layout\_debug\_sdk.yml

Before analyzing the layout file, developers need to understand the concept of partition table. The partition table is actually a binary file, which stores the name, attribute, address, size and other information of each firmware. Before booting app or ota\_app, SSB also need to obtain the address of the firmware to be booted from the partition table. The content of the partition table is determined by the layout file. The format of the layout file is as shown in Figure 11.

```
active_partition: 0
layout_version: 0x52
app_type: 0 # 0-
partition_table_addr: 0x7000
entpias:
                                               The real starting address of the app
       name: "app"
type: firmware
                                             ₹16-byte image header
        size: 0x41c00 # (263 KB) 0x41c00 + 0x1400(persdata) = 0x43000
           fsets:
- 0x0
           - 0x23c10
           - 0x35410
            - 0x39c10
  - name: "ota"
type: firmware
                                           The real starting address of the
       base_address: 0x501f0
size: 0x22d00 # 140k KB
       offsets:
- 0x0
             - 0x0 # 1pc_ota_app | 80 KB
- 0x13e10 # nxh_ota_app | 60 KB
     - name: "app_data"
        type: appdata
        size: 0x400 # 1 KB
       offsets:
          ame: "nxh_pers_data"
        type: appdata
base_address:
                     ess: 0x4ac00
       offsets:
- 0x0
    name: "app_data_factory"
type: factoryreset
base_address: 0x4b000
             - 0x0
     - name: "nxh_pers_factory"
        type: factoryreset
base_address: 0x4b400
        offsets:
- 0x0
```

The page size of LPC5528's flash is 512 bytes, but as shown in Figure 11, the starting addresses of app and ota\_app are not

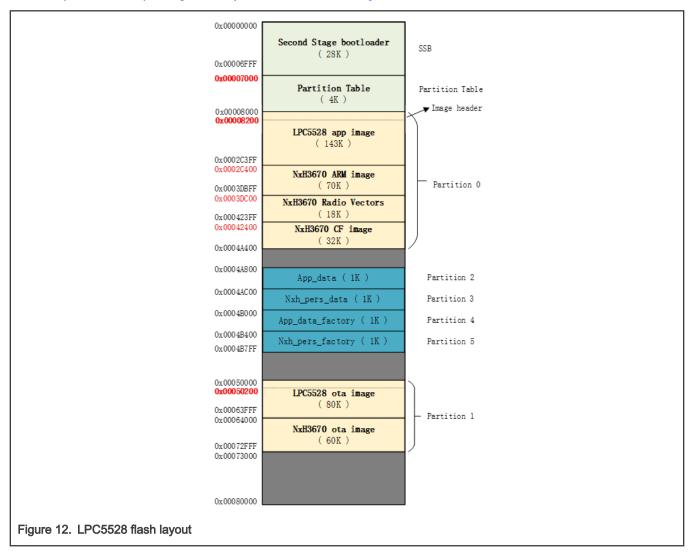
Figure 11. Format of the layout file

aligned with 512 bytes. They are actually the start addresses that contain the 16-byte image header. The real firmware starting addresses are 0x8200 and 0x50200 respectively. Since LPC5528's Flash sector size is 32 KB, in order to manage the storage space of app and ota\_app more conveniently, app and ota\_app are stored in different sectors.

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The address in the linker file of the Keil project needs to meet 512 bytes alignment and at least 16 bytes of space is reserved for the imager header, so 512 bytes are reserved for the imager header and the first 496 bytes are filled with 0. Therefore,  $0 \times 8000 - 0 \times 81 \text{FF}$  and  $0 \times 50000 - 0 \times 501 \text{FF}$  are composed of 496 bytes,  $0 \times 00$ , and 16 bytes image header.

The flash partition corresponding to this layout file is as shown in Figure 12.



# 3.1.2 Flashlist\_debug\_sdk.yml

The content of flashlist file is as shown in Figure 13.

```
- name: ssb
 files:
                     - lpc_democode/apps/lpc5528_gamepad_ssb/debug/lpc5528_gamepad_ssb.bin
                 address: 0x0
               - name: lpc_app
                     - lpc_democode/apps/lpc5528_gamepad/debug/lpc5528_gamepad.bin.eep
                    tition: { name: "app", type: firmware }
               - name: nxh_app

    nxh3670_binaries/arm/phGamingRx.ihex.eep

                 partition: { name: "app", type: firmware }
                 headroom: 4096
               - name: rfmac
                    - nxh3670_binaries/rfmac/rfmac.eep
                partition: { name: "app", type: firmware }
headroom: 2048
               - name: cf

    nxh3670_binaries/cf/phStereoInterleavedAsrcRx.eep

                 partition: { name: "app", type: firmware }
                 headroom: 2048
               - name: lpc_ota_app
                     - lpc_democode/apps/lpc5528_gamepad_ota/debug/lpc5528_gamepad_ota.bin.eep
                offset_index: 0
partition: { name: "ota", type: firmware }
               name: nxh_ota_app
                     - nxh3670_binaries/arm/ph0taHeadset.ihex.eep
                 offset index: 1
                 partition: { name: "ota", type: firmware }
Figure 13. flashlist_debug_sdk.yml
```

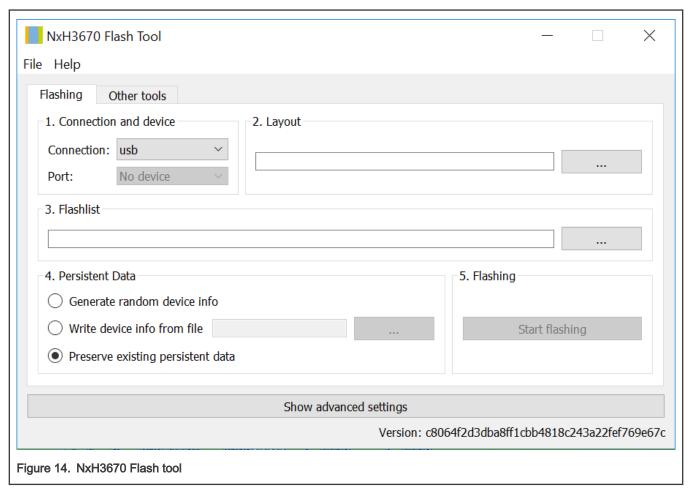
The flashlist file provides the names and relative paths of some necessary firmware. In addition to downloading the firmware in Figure 13, you also need to download the partition table and device information (NxH\_pers\_data) to the specified address of LPC5528.

## 3.2 GUI tools

In order to facilitate developers to debug and update programs, the LPC5528\_NxH3670 SDK also provides three tools, namely NxH3670 Flash tool, LPC5528\_NxH3670 Flash tool and VCOM tool. Among them, NxH3670 Flash tool and VCOM tool come from original NxH3670 SDK Rev5.2.

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#### 3.2.1 NxH3670 Flash tool



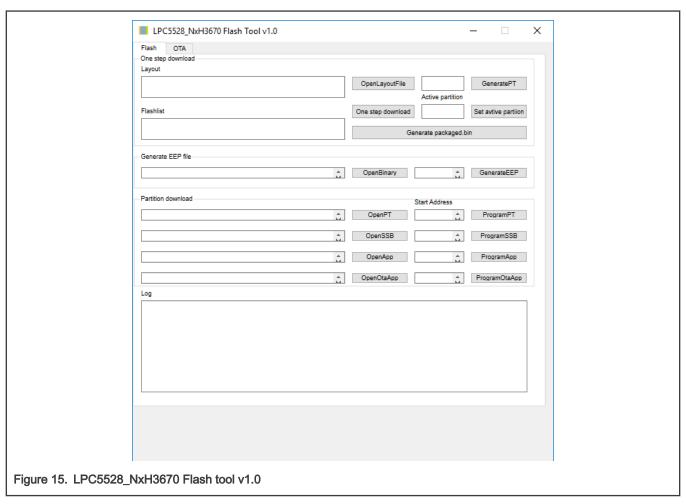
There are two main functions supported by this tool:

- · Host controller program download
- · OTA upgrade

When using this tool to download the host controller program, the tool currently does not support the program download of LPC5528, so an LPC5528 NxH3670 Flash tool GUI is re-developed in this solution.

## 3.2.2 LPC5528\_NxH3670 Flash tool

The GUI interface of LPC5528\_NxH3670 Flash tool is as shown in Figure 15.



This tool implements the following nine functions. Except **Generate partition table**, **Generate EEP**, and **Generate packaged.bin**, when using other functions, LPC5528 need to enter the ISP mode.

## 3.2.2.1 Generate partition table

This function is used to generate partition table.

- 1. Click the OpenLayoutFile button to select the correct layout file.
- 2. Click the **GeneratePT** button to generate a partition table file and it will be saved in same directory.

#### 3.2.2.2 Set active partition

This function is used to set active partition.

- 1. Select the correct layout file and enter a valid partition value in the **Active partition** box.
- 2. Click the **Set active partition** button. The GUI tool will first read out the partition table that already exists in the flash and modify the active partition value, and then write the new partition table to the flash.

## 3.2.2.3 Generate EEP

This function is used to generate an EEP file with an image header.

- 1. Click the **OpenBinary** button and select the *bin* file.
- 2. Click the **GenerateEEP** button to generate the EEP file and save it in the same directory. In addition to using this function to generate EEP files, you can also use the after-build command in the Keil IDE.

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For the details, see Generate binary and EPP file.

### 3.2.2.4 Program partition table

This function is used to download the partition table.

- 1. Click the **OpenPT** button.
- 2. Select the partition table file and enter the download address in the Start Address window.
- 3. Click the ProgramPT button to download the partition table.

## 3.2.2.5 Program SSB

This function is used to download SSB firmware.

- Click the OpenSSB button.
- 2. Select the SSB file and enter the download address in the Start Address window.
- 3. Click the ProgramSSB button to download the SSB.

## 3.2.2.6 Program App

This function is used to download App firmware.

- 1. Click the **OpenApp** button.
- 2. Select the app.eep file and enter the download address in the Start Address window.
- 3. Click the **ProgramApp** button to download the app firmware.

#### 3.2.2.7 Program Ota App

This function is used to download the ota\_app firmware.

- 1. Click the OpenOtaApp button.
- 2. Select the ota app file and enter the download address in the Start Address window.
- 3. Click the ProgramOtaApp button to download the ota app firmware.

### 3.2.2.8 Generate packaged binary

This function is used to generate packaged binary.

- 1. Select a layout file, and the GUI will automatically select the flashlist file in this directory.
- 2. Click the **Generate packaged.bin** button. The GUI parses the selected layout file and generates a partition table binary, and then merges the SSB, *app.eep*, *ota\_app.eep*, partition table, and device information files into one *\_packaged.bin* file according to the firmware path provided in the *flashlist* file.

#### 3.2.2.9 One step download

This function is used for one-step download.

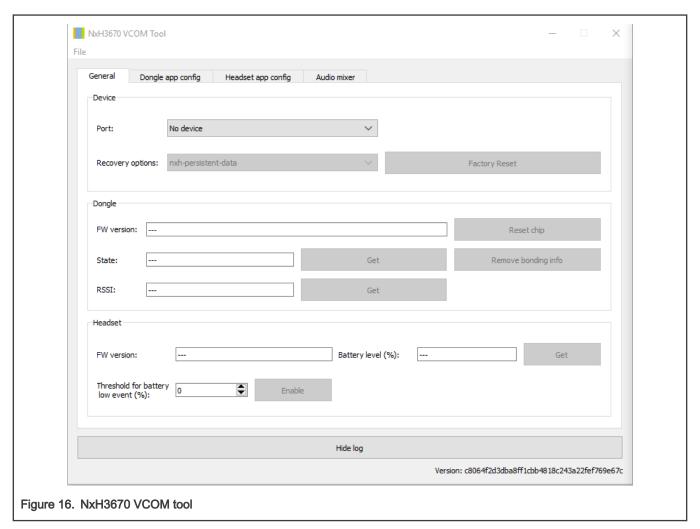
- 1. Click the Generate packaged.bin button to generate a \_packaged.bin file containing multiple firmware.
- 2. Click the One step download button to download multiple firmware to the address specified by the layout file.

If a *packaged.bin* has been generated and does not need to be updated, you can directly click the **One step download** button to download the complete firmware to the flash.

#### 3.2.3 VCOM tool

This solution also provides a NxH3670 VCOM tool, and its GUI interface is as shown in Figure 16.

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For the usage of NxH3670 VCOM tool, see NxH3670-0032\_UM\_HAPI\_VCOM\_Rev0.6.pdf in NXH3670 SDK Rev5.2.

# 3.3 Project debug

This section describes how to debug each project in the *lpc\_democode* directory.

# 3.3.1 Configuration of Keil projects

When using Keil for SSB, app and ota app development, some special project configurations are required.

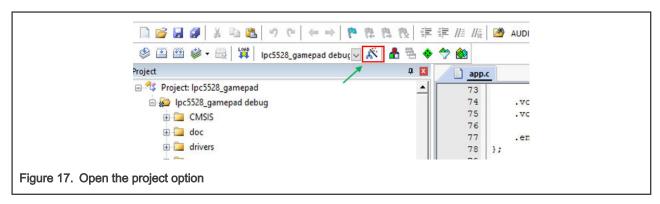
## 3.3.1.1 Generate binary and EPP file

When developing app and ota\_app, you need to configure after build command to generate eep files. The steps are as follows:

1

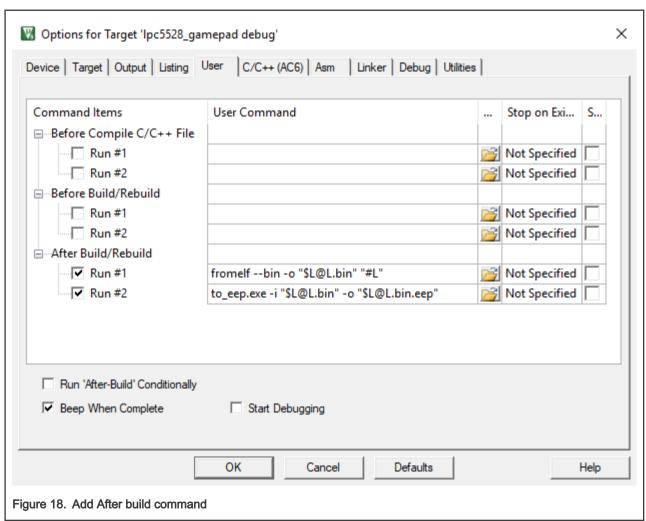
- 1. Open the lpc5528\_gamepad project or ota\_app project in the *lpc\_democode/apps* directory.
- 2. Click the

button, open the project option.



3. Switch to the user window and add two commands to the **After build/Rebuild** column, as shown in Figure 18. These two commands will be executed automatically every time the project is compiled.

The first command is to generate a .bin file and the second command is to generate a .eep file.



The second command needs to use the *to\_eep.exe* file, which is stored in the */bin/windows* directory of the LPC5528\_NxH3670 SDK package. Before executing this command, you need to copy *to\_eep.exe* to the Keil installation directory, *../ARM /ARMCLANG/bin*. The phenomenon after execution is as shown in Figure 19.

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The eep file actually adds a 16-byte image header to the head of the binary file, as shown in Figure 20.

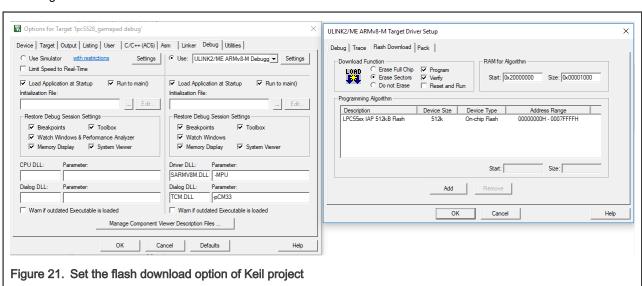
What is downloaded to LPC5528 through LPC5528\_NxH3670 Flash tool is the .eep file. When using Keil to debug the project directly, the binary file without the image header is downloaded.

As shown in Figure 20, the image header has a total of 16 bytes, including Image length, type, starting address and checksum. When performing an OTA upgrade, the dongle will read the header of the image to be upgraded in the remote (Gamepad) flash and compare it with the new image header. If the image headers are the same, they are considered the same image, and the dongle will skip the upgrade of this image, this image will not be sent to the gamepad. If they are different, a new image will be sent to the remote to replace the old image.

#### 3.3.1.2 Sector erase

The flash of the LPC5528 needs to contain multiple firmware, so when debugging a single project, you need to set the download function to **Sectors Erase**. The contents of other firmware are retained when a single program is downloaded. The steps are as follows:

- 1. Follow Step 1 and Step 2 in Generate binary and EPP file.
- Switch to the Debug window and click Settings -> Flash Download. Select Erase Setcors and click OK to save the configuration.



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## 3.3.2 Debugging single project

Before debugging the app or ota\_app separately, make sure that the SSB and partition table have been downloaded in the LPC5528 flash. SSB is the first code executed after MCU reset, it will read the partition table to obtain the active partition value and then jump to the corresponding partition.

### 3.3.2.1 Download SSB

There are three ways to download SSB:

- Open the SSB project, compile the SSB project, and click the **Download** button in the project to directly download the SSB to 0x00 in the flash.
- Use the Program SSB function in the LPC5528\_NxH3670 Flash tool to download the SSB firmware. For details, see Program SSB.
- Use the One step download function to download SSB. For details, see One step download.

#### 3.3.2.2 Download Partition table

There are two ways to download the partition table:

- Use the Program PT function to download the Partition table. For details, see Program partition table.
- Use the One step download function to download the partition table. For details, see One step download.

After downloading the SSB and partition table, you can debug the app or ota app project separately.

- When debugging the app project, set activie parition in the layout file to 0.
- When debugging ota app, set activie parition to 1.

Once modifying the layout file, you need to regenerate the partition table and download the new partition table to the flash.

# 4 Gamepad usage

After downloading the firmware of the dongle and gamepad using the **One step download** function, reset the two boards and make both boards run in app mode. By default, the gamepad and dongle work in the wireless mode. They will automatically complete the pairing process. You can switch the working mode by pressing a specific button and adjust the volume. The names of the buttons are as shown in Figure 22.

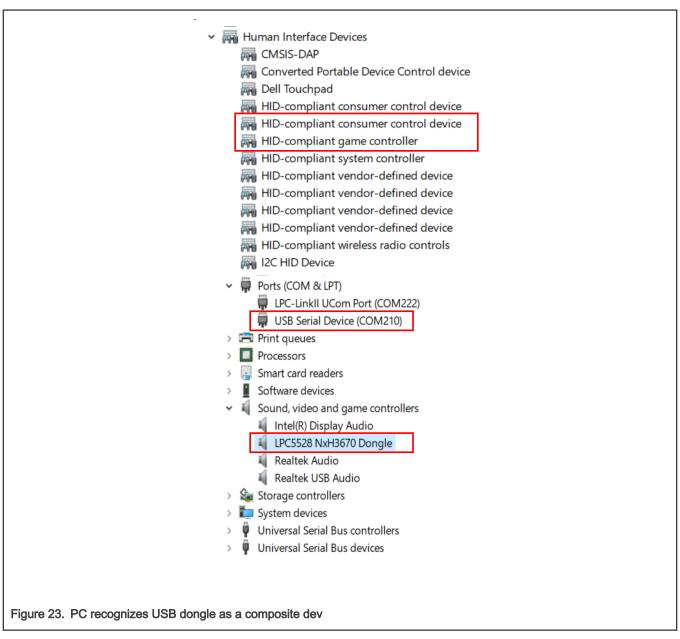
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Figure 22. Button name of gamepad

# 4.1 Wireless mode

Plug the USB dongle into the PC, the red and green led will light up, and a composite USB device will be recognized on the PC, as shown in Figure 23.

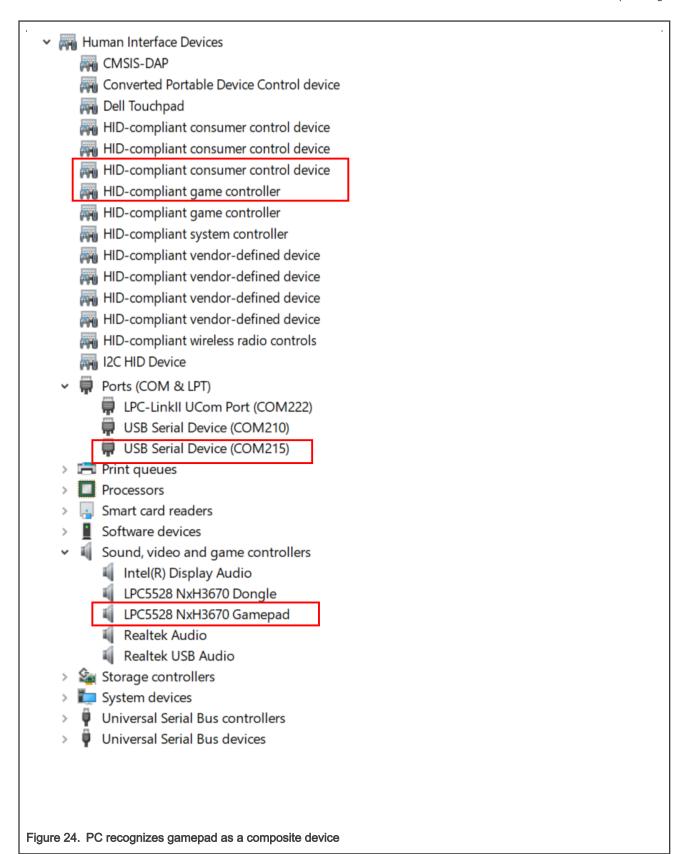


As shown in Figure 23, the USB dongle is a composite device integrating USB CDC + USB audio + USB HID + USB vendor specific class.

- Check the status of the red and green LED on the gamepad main board. If the red and green LED are OFF, it indicates that gamepad is in a low-power state or the lithium battery is low.
- Press the **START** button to try to wake up the gamepad in the low-power mode. If the red and green LED are ON, it indicates that the gamepad wakes up from the low-power mode and enters the normal run mode. By default, the gamepad works in the wireless mode. It will actively initiate the pairing process with the dongle. If the pairing is successful, the red LED on the dongle and gamepad main board will both be OFF. If the red and green led on the gamepad main board are still OFF after pressing the **START** button, it indicates that the gamepad's lithium battery is low. You need to use a micro USB cable to connect the PC and the gamepad, using the PC to charge the lithium battery. After the gamepad is started normally, the PC will recognize a composite USB device, as shown in Figure 24. Then gamepad will automatically initiate the pairing process with dongle. If the pairing is successful, the red LED on the dongle and gamepad main board will both be OFF. If the pairing process fails, the red LED on the gamepad is always ON and the gamepad is switched to the wired mode.

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- Press the XE + SATRT buttons to switch the working mode between wired and wireless mode.
- Press the XE + UP/DOWN buttons to adjust the volume of the gamepad.

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For more details about gamepad operation, see LED state to Low-power mode.

## 4.2 Wired mode

Connecting the PC and the gamepad with a micro USB cable and no need to use LPC5528 USB dongle board. After the gamepad is started normally, the PC will recognize a composite USB device, as shown in Figure 24. The gamepad will still initiate the pairing process with the dongle first, but the pairing process will fail because there is no dongle exist, and the gamepad automatically switches to wired mode.

### 4.3 LED state

Table 1 describes the LED state of gamepad main board.

Table 1. LED state of gamepad main board

Work mode		RED	GREEN
Run mode	Wired	ON	ON
	Wireless	OFF	ON
Low power mode		OFF	OFF

Table 2 describes the LED state of dongle board.

Table 2. LED state of dongle board

Work mode		RED	GREEN
Run mode	Wired	ON	ON
	Wireless	OFF	ON

## 4.4 Volume control

• Volume UP: XE + UP

Volume Down: XE + DOWN

## 4.5 Mode switch

Press the XE button and then the START button to switch between wired mode and wireless mode.

#### 4.6 Software reset

Press the XE button and hold it for ten seconds.

## 4.7 Low-power mode

If no button event is detected for five minutes, gamepad will enter the deep-sleep mode. To wake up the gamepad, press the **START** button.

## 5 Useful document

When developing this solution, documents provided in NxH3670 SDK Rev 5.2, as listed in Table 3, were referenced.

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Table 3. Documentation in NXh3670 SDK Rev5.2

Key words	Document name
OTA	NxH3670_AN12361_OTA_Firmware_Updrade.pdf
OTA	NxH3670_UM11203_HAPI_OTA.pdf
VCOM tool	NxH3670-0032_UM_HAPI_VCOM_Rev0.6.pdf
Software Architecture	NxH3670_AN12280_Reference_Application_Software_Architecture.pdf
Performance	NxH3670_0029_AN_Performance.pdf
	NxH3670_AN11953_HAPI_Bootloader.pdf
Host API	NXH3670_HAPI_overview.pdf
	NxH3670_UM11148_HAPI_Gaming.pdf
	NxH3670_UM11149_Host_Interface.pdf
Flash Tool	NxH3670_UM11198_FlashTool.pdf

These documents cannot be downloaded separately on the NXP website and they are stored in the NxH3670 SDK Rev 5.2 package. For more development information, please refer to the complete document in the NxH3670 SDK.

# 6 Reference

- Getting started with NxH3670 gaming use case (document AN12360)
- Over-the-air firmware update (document AN12361)
- NxH3670 Reference Application Software Architecture (document AN12280)
- NxH3670 Performance
- NxH3670 HAPI OTA (document UM11203)
- NxH3670 HAPI VCOM
- NxH3670 FlashTool (document UM11198)

# 7 Revision history

Rev.	Date	Substantive changes
0	16 December, 2020	Initial release
1	21 May, 2021	Updated Figure 1, Figure 2, Figure 23 and Figure 24

Application Note

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